



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/879,335  
Filing Date ..... June 11, 2001  
Inventor ..... Vishnu K. Agarwal, et al  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Y. Huynh  
Attorney's Docket No. .... MI22-1568  
Title: Capacitor Forming Methods and Capacitor Constructions

#8/cited  
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7/23/02

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

REMARKS

The citations listed may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §1.56. The Examiner is requested to make these citations of official record in this application. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation-in-part application of co-pending application Serial No. 09/710,546, filed November 8, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned.

Respectfully submitted,

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Dated: 01 Jul 2002

Attorney: James E. Lake

Reg. No.: 44,854

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